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Reduced-Scale Transition-Edge Sensor Detectors for Solar and X-Ray Astrophysics

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Abstract—We have developed large-format, close-packed X-ray microcalorimeter arrays fabricated on solid substrates, designed to achieve high energy resolution with count rates up to a few hundred counts per second per pixel for X-ray photon energies up to 8 keV. Our most recent arrays feature 31-micron absorbers on a 35-micron pitch, reducing the size of pixels by about a factor of two. This change will enable an instrument with significantly higher angular resolution. In order to wire out large format arrays with an increased density of smaller pixels, we have reduced the lateral size of both the microstrip wiring and the Mo/Au transition-edge sensors (TES). We report on the key physical properties of these small TESs and the fine Nb leads attached, including the critical currents and weak-link properties associated with the longitudinal proximity effect.

Index Terms—Arrays, low temperature detectors, microcalorimeters, transition-edge sensors (TES), X-ray spectroscopy.

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I. INTRODUCTION

WE HAVE been developing small-pixel x-ray microcalorimeters for solar physics applications [1]–[3]. The basic requirements for this application are that the pixels are small and fast (with decay times less than a few tens of microseconds), can be fabricated into large arrays of close-packed pixels, and have a realizable read-out scheme. The previous generation featured absorbers $65\ \mu\text{m}$ square on a $75\ \mu\text{m}$ pitch, attached to Mo/Au bilayer transition-edge sensors (TESs) $36 \times 3\ \mu\text{m}$ in size. In the current generation, the pixel spacing is reduced to $35\ \mu\text{m}$, while the sensors are fabricated with dimensions as small as $7\ \mu\text{m}$ across. These changes increase the areal pixel density by a factor of four while revealing device behaviors relating to the longitudinal proximity effect.

The basic geometry of these devices is depicted in Fig. 1. Each detector pixel consists of an X-ray absorber in strong thermal contact with a sensitive TES thermometer. The cantilevered absorber is supported by only a single round stem, one micron in diameter, located in the center of the TES. The transition-edge sensor is a superconducting bilayer of molybdenum and gold that can be biased in-between its superconducting and normal metal states, where any small change in temperature is transduced into a measurable current. The properties of these small TESs are determined by the dependence of the critical current on temperature, magnetic field, and size. Many of these properties have been explained by the junction-like properties of the weak link formed by the TES sensor and the superconducting leads attached, forming an S-N'-S weak-link. [4]

II. ARRAY DESIGNS

A key aspect to the development of compact arrays is the ability to route the fine pairs of wires carrying the TES current with high density. It requires a minimum of eight pairs of wires between each pixel at the outer edge in order to wire out all of the pixels comprising a 32×32 array. [5] The previous generation utilized a microstrip structure with a $4\ \mu\text{m}$ pitch. [1] The widths of the lower (Lead 1) and upper (Lead 2) conductors were $2.5\ \mu\text{m}$ and $1.5\ \mu\text{m}$, respectively, with a gap between lines of $1.5\ \mu\text{m}$. Using optical reduction lithography with the capability to resolve structures down to $280\ \text{nm}$ and an overlay accuracy of $40\ \text{nm}$, these dimensions have now been reduced to a $2\ \mu\text{m}$ pitch, with lead widths of $1\ \mu\text{m}$ and $0.5\ \mu\text{m}$, producing

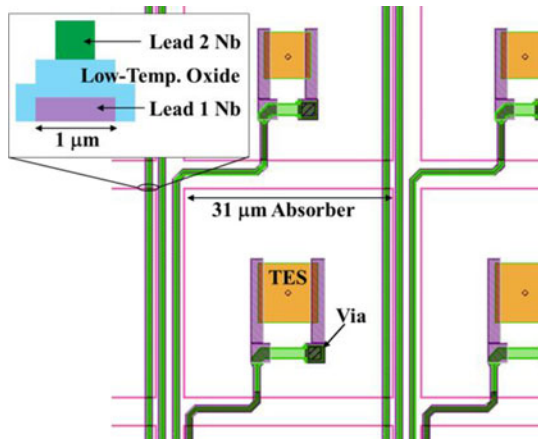


Fig. 1. Schematic of the small pixel array design showing TESs with microstripped leads, vias, stems, and 31 μm absorbers. Inset: microstrip structure in cross-section.

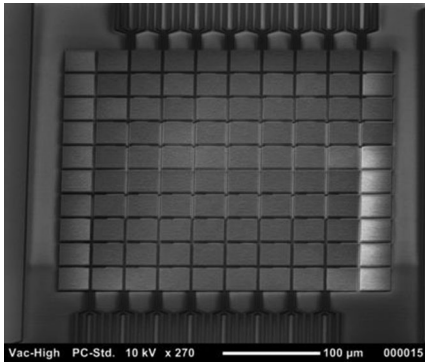


Fig. 2. 10 \times 10 absorber-coupled TES array with 2 μm thick absorbers on single stems 1 μm in diameter.

a 1 μm gap. The reduced scale would suffice to wire out a 48 \times 48 array on a 35 μm pitch, exceeding 2300 total pixels. A single detector chip contains 64 wired-out sensors with four TES dimensions: 7, 8, 9, and 12 μm . The 10 \times 10 test array is shown in Fig. 2.

The absorbers are electroplated gold 31 \times 31 \times 2 μm , with a gap between absorbers of 4 μm . Future development will focus on increasing the thickness to 4 μm . In these prototype detectors there are no normal metal banks along the two edges between the TES contacts, and also no normal metal stripes such as have been empirically shown to reduce unexplained detector noise in larger TESs [6]. The absence of these features simplifies the analysis of experimental results. The array, although deposited on a silicon wafer covered with layers of thermal oxide and low-stress LPCVD silicon nitride, has not been released and sits on a solid substrate rather than on a membrane. There is also no embedded heat sink layer, which is typically employed to reduce thermal cross-talk between pixels [7]. In future we will use a back-etch to make the membranes underneath the pixels free-standing, and then deposit a film of copper onto the back-side of the chip to provide the heat bath, as has been employed in some other prototype arrays [8].

We discuss in this report results from two fabrication runs. Example TESs from each run, denoted “A” and “B”, are shown

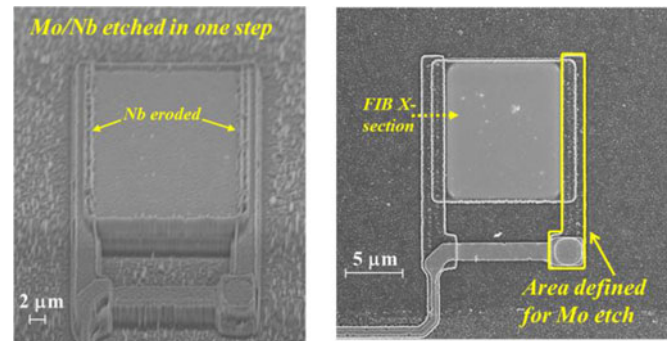


Fig. 3. (a) Completed TES device “A”. The Nb leads are eroded and overlap the TES by less than 0.5 μm on each side. The TES is covered by a layer of low-temperature deposited silicon dioxide. (b) Completed TES device similar to “B”. The Nb leads overlap the TES by a full 1 μm , as designed; the SiO_x layer has been removed.

in Fig. 3. The “B” fabrication process included cantilevered absorbers, while for “A” it did not. Results from testing of these devices suggest differences in the interface structure and geometry between the Mo/Nb leads and the Mo/Au TESs, described in the next section.

III. FABRICATION

The devices are fabricated on a silicon wafer 300 μm thick, coated with layers of thermal silicon dioxide and low-stress LPCVD silicon nitride. The Mo/Au (approximately 50 nm/220 nm) bilayer is removed from the wafer perimeter using a wet etch to facilitate the subsequent patterning and etching of alignment marks for optical reduction lithography. The Mo/Au TESs, 7–12 μm in size, are patterned by optical reduction lithography and etched by argon ion milling. By simple timing it is possible in the load-locked ion mill system to reliably remove the gold layer while halting only partway through the molybdenum layer below. The “A” and “B” fabrication processes diverged at this point. For “A”, deposition of the 200 nm thick Lead 1 Nb layer followed next. It was therefore necessary for the reactive ion etch (RIE) defining Lead 1 to remove the Nb and Mo layers together, under the same photoresist mask. The etch slowed on the Mo layer, requiring a significant over-etch that eroded the niobium layer on top of the TES. This issue was addressed for “B” by patterning the Mo layer to define and remove regions adjacent to the TES prior to Lead 1 deposition, as indicated in Fig. 3(b).

A focused-ion beam (FIB) cross-section of side-by-side completed microstrip structures from “B” is shown in Fig. 4(a). Registration between the Lead 1 and Lead 2 layers is consistent with the 40 nm overlay specification. Measurement reveals critical currents for the Mo/Au bilayer of 548 μA at 475 mK and 960 μA at 200 mK, which exceeds the operational requirement by a significant margin. The critical current density in the Lead 2 layer therefore exceeds $1.0 \times 10^6 \text{ A/cm}^2$.

The cantilevered absorbers are fabricated using seed layer deposition over a photoresist layer defining the absorber “stems”, followed by plating and ion milling to define the absorber “hood”. Success requires that the photoresist mold for the stem have a sloped, positive profile. While this may be achieved by

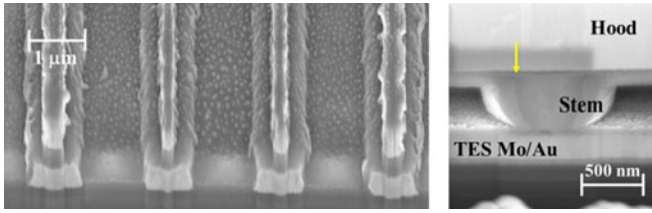


Fig. 4. FIB cross-sections: (a) Microstrip structures with 1 μm and 0.5 μm (nominal) lower and upper conductors, separated by a 300 nm thick dielectric layer. (b) Cantilevered absorber with a 1 μm diameter stem, fabricated using a defocus setting of $-1.36 \mu\text{m}$. The interface between the evaporated and plated gold layers in the stem is indicated with an arrow.

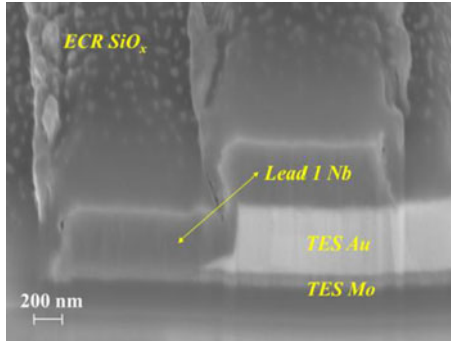


Fig. 5. FIB cross-section of the TES-lead interface for a “B” device.

reflowing the photoresist at an elevated temperature, the approach is not suitable at stem dimensions of $1\text{ }\mu\text{m}$ and smaller. We employed an alternative approach for “B” in which the mask image of the stem is slightly defocused during exposure using optical reduction lithography. The method has been demonstrated to create a sloped resist profile. [9] A matrix of defocus settings between $-5.0\text{ }\mu\text{m}$ and $+5.0\text{ }\mu\text{m}$ was explored to select the proper value for this application by trial and error. Values between $-0.80\text{ }\mu\text{m}$ and $-1.36\text{ }\mu\text{m}$ were identified as promising; absorber test structures with stems as small as $0.75\text{ }\mu\text{m}$ yielded successfully. Fig. 4(b) shows the cross-section of an absorber fabricated using this approach.

IV. INTERFACE CHARACTERIZATION

In Fig. 5 we present an FIB cross-section of the TES-lead interface for a “B” device. The Lead 1 niobium layer overlapping the TES is whole, undamaged, and in good contact with both the gold underneath it and with the adjacent Mo/Nb lead. The overlap dimension is $1\text{ }\mu\text{m}$, so that the lead spacing L is two microns less than the TES dimension. The edge of the TES is somewhat sloped, which is desirable for step coverage, with a tail extending about 200 nm underneath Lead 1. At the edge of the lead, the niobium structure is aligned very well to the molybdenum layer, which was patterned and etched in a separate step. In profile, the Lead 1 niobium is nicely sloped. A positive profile is necessary for fabrication of a high-quality via. In summary, the fabrication process yields the desired physical structure, with excellent size resolution and overlay.

Examination of the “A” devices reveals that the niobium on top of the TES gold is eroded, as indicated by the image of Fig. 3(a). Therefore the lead spacing for “A” devices is only one

TABLE I
TRANSITION TEMPERATURE MEASUREMENTS

Lead Spacing L (μm)	“A” T _C (mK)	“B” T _C (mK)
5		> 1200, > 1200*
6	460, 480	1000, 1050, > 1200*
7	530, 560	> 1200
8	370, 370	
10		790
11	260	
T _{Ci} (mK)	164	84
R _{TES} (m Ω)	27	12.5

Table 1 Transition temperature measurements under 10 μ A excitation for “A” and “B” devices. T_{Ci} is the intrinsic transition temperature of the Mo/Au bilayer material. “B” measurements on a device with no absorber are denoted with an asterisk.*

micron less than the TES dimension. The “A” devices nevertheless exhibit good Mo-Nb contact in the lead, with a sloped TES sidewall.

V. ELECTRICAL CHARACTERIZATION

Table 1 shows measurements of the transition temperature (T_C) for “A” and “B” devices. T_C indicates the beginning of the superconducting transition as measured using SQUID electronics while slowly increasing the base temperature. The applied bias is a 10 μ A triangular wave with a frequency of 2 Hz. Seven devices were tested from both “A” and “B” fabrication runs, each covering all four sizes of TES devices. Two devices from “B” had no absorbers. The intrinsic transition temperature of the Mo/Au bilayer material was 164 mK for “A” and 84 mK for “B”, in each case measured on a Mo/Au test structure using a four-wire method.

Device normal resistances were approximately 27 m Ω and 12.5 m Ω , respectively. Due to the longitudinal proximity effect, the niobium leads sustain superconductivity in the Mo/Au TES far above the intrinsic transition temperature (T_{ci}) exhibited by the bilayer. The effect is found for both “A” and “B”. The effect is smaller for “A” either due to erosion of the niobium lead where it overlaps the TES, or because of the higher normal state resistance of the TES bilayer.

The data indicate that the T_C enhancement does not decline monotonically with lead spacing. For both “A” and “B”, devices with $L = 7 \mu\text{m}$ exhibited a higher critical temperature than devices with $L = 6 \mu\text{m}$ lead spacing. An explanation for this observation is currently lacking. There also appears to be a meaningful discrepancy between the critical temperature measured for “B” devices with and without absorbers, which is attributable to the effect of the absorber stem in contact with the TES surface.

The critical current as a function of temperature for a $7\text{ }\mu\text{m}$ device from “B” ($L = 5\text{ }\mu\text{m}$), a $7\text{ }\mu\text{m}$ device from “A” ($L = 6\text{ }\mu\text{m}$), and previously published data from a TES device of a different design with $8\text{ }\mu\text{m}$ lead spacing [4] is plotted in Fig. 6. The published data has been shown to fit well to a theory derived from the Ginzburg-Landau equations, exhibiting an exponential dependence on L . The data from an “A” device correspond reasonably closely to the previously published data, which is expected since the values of the intrinsic transition temperature

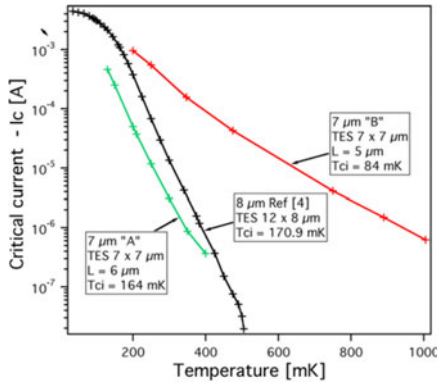


Fig. 6. Critical currents of “A” and “B” devices compared to a previous result for an $L = 8 \mu\text{m}$ TES device. [4] None of the devices have absorbers.

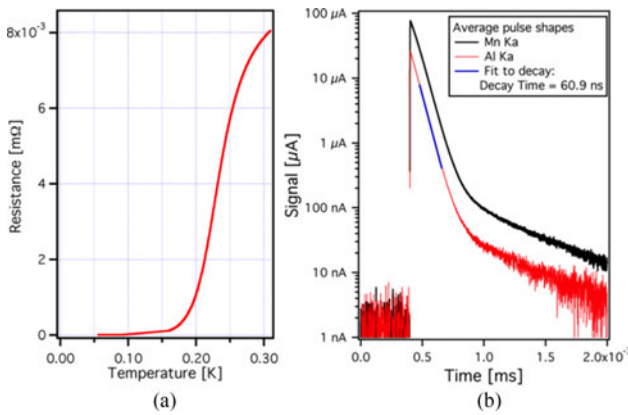


Fig. 7. (a) Resistance versus temperature curve for a $12 \mu\text{m}$ device ($L = 10 \mu\text{m}$), and (b) pulse shapes for 1.5 and 5.9 keV X-rays, measured using a $9 \mu\text{m}$ TES ($L = 7 \mu\text{m}$) with a $31 \mu\text{m}$ absorber, both at a bath temperature of 55 mK.

(170.9 mK vs. 164 mK) and lead spacing are very similar. At 60 mK, the $L = 6 \mu\text{m}$ “A” device exhibits $I_C = 0.95 \text{ mA}$; for the $L = 5 \mu\text{m}$ “B” device, $I_C = 5.7 \text{ mA}$ at 55 mK. Compared to the “A” device, it is clear that the “B” device exhibits a much greater critical current over a broad temperature range.

VI. X-RAY MICROCALORIMETER PERFORMANCE

We have made first measurements of a “B” pixel as an X-ray microcalorimeter using a $9 \mu\text{m}$ TES ($L = 7 \mu\text{m}$). Devices of other dimensions have not yet been tested. The resistance versus temperature curve under test conditions for a bath temperature of 55 mK is shown in Fig. 7(a). It was determined from an I-V measurement of the TES, assuming that the thermal conductance is given by the thermal boundary resistance as calculated by Lee *et al.* [10]. The normal resistance was $12.5 \text{ m}\Omega$. The TES was then biased at 2.5% of the TES normal resistance ($0.31 \text{ m}\Omega$). The energy sensitivity was determined from the average signal response to 1.5 keV Al $K\alpha$ X-rays, measuring the average noise spectrum and then integrating the noise equivalent power [11].

As can be seen in Fig. 7(b), the signal for the Al $K\alpha$ X-rays has a single exponential decay for most of the pulse, followed by a second longer decay of low amplitude. The first decay time constant is 60 ns. The second decay is likely related to a

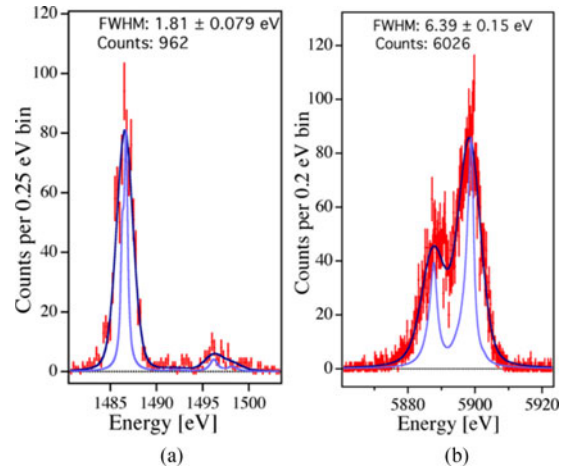


Fig. 8. Measured energy spectrum of (a) Al $K\alpha$ complex and (b) Mn $K\alpha$ complex. The dark blue curves are the best fits to the spectrum, and the light blue curves are the line shapes used.

small amount of heating of the substrate since this first prototype detector does not yet incorporate a layer of heat-sinking copper underneath it, separated by an insulation layer. The signals from 6 keV X-rays only show a small amount of non-linearity even though the temperature rise from these X-rays is expected to be approximately 30 mK, since the $31 \mu\text{m}$ absorber is just $2 \mu\text{m}$ thick.

Spectra acquired from two different energies, and from two different X-ray sources are shown in Fig. 8(a) and (b). Fig. 8(a) shows a measurement of fluorescent Al $K\alpha$ X-rays from a Manson source. The energy resolution achieved corresponds to the broadening of the intrinsic Al $K\alpha$ X-rays by a microcalorimeter energy resolution of 1.8 eV (FWHM), consistent with best achievable resolution.

Fig. 8(b) shows worse energy resolution for Mn $K\alpha$ X-rays from an ^{55}Fe radioactive source of around 6.4 eV (FWHM). The origin of this broadening is not yet understood. Possibly it arises because the prototype device has a thin absorber, which thermalizes slowly compared to the fast pulse decay time. Athermal phonon loss is not expected to be a problem [12] because the area of the contact region between the absorber and the TES is only $\sim 0.1\%$, far less than has previously been shown to be needed for athermal phonon loss to be a problem at this resolving power [13].

VII. CONCLUSION

We have developed the first X-ray microcalorimeter arrays on a pitch as fine as $35 \mu\text{m}$. The basic design and operation was successful, demonstrating the high potential for this type of X-ray microcalorimeter for future solar and X-ray astrophysics instruments. The new fabrication techniques used to create extremely fine-pitch microstrip lines, the very small TESs, and the attachment of absorbers through $1 \mu\text{m}$ stems, have all successfully been demonstrated for the first time. Measurements indicate that the TES properties appear highly dependent on the interface geometry between the TES and its leads.

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